











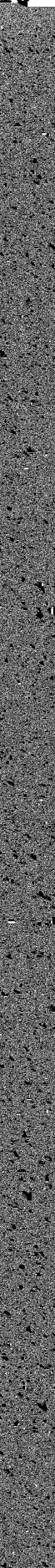
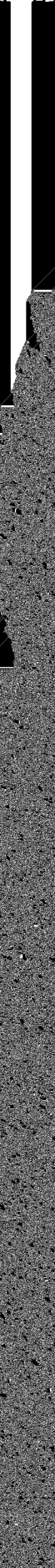
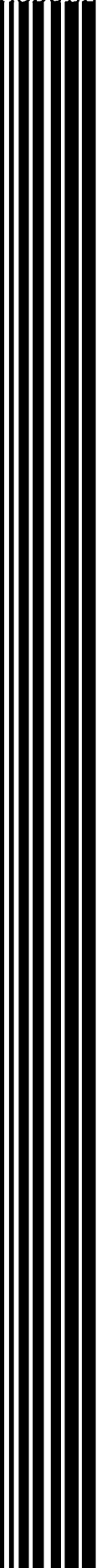
MODULE - Digital Electronics  
SUB-MODULE - Binary Logic

COMPETENCIES TO BE DEVELOPED

Upon satisfactory completion of this sub-module each

MODULE

- Digital Electronic





















TOPIC - Combinational Logic

MODULE - Digital Electronics

SUB-MODULE - Binary Logic



TOPIC - Combinational Logic      MODULE - Digital Electronics  
SUB-MODULE - Binary Logic

PERFORMANCE OBJECTIVE No. 2



TOPIC - Combinational Logic

MODULE - Digital Electronics  
SUB-MODULE - Binary Logic

PERFORMANCE OBJECTIVE No. 3

(Enrichment)

Given a Boolean expression for a combinational logic





















MODULE - Digital Electronics  
SUB-MODULE - Logic Systems

TOPIC - Sequential Logic Circuits











MODULE - Digital Electronics  
SUB-MODULE - Logic Systems

TOPIC - Sequential Logic Circuits

5. Home study - have students complete practice calculations in binary addition and subtraction. Compare rules to truth tables for XOR and XNOR gates.
6. Computer assisted instruction on binary calculations.
7. Research assignment - have students investigate and report in verbal or written form on the application of binary



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MODULE - Digital Electronics  
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